

Figure 1

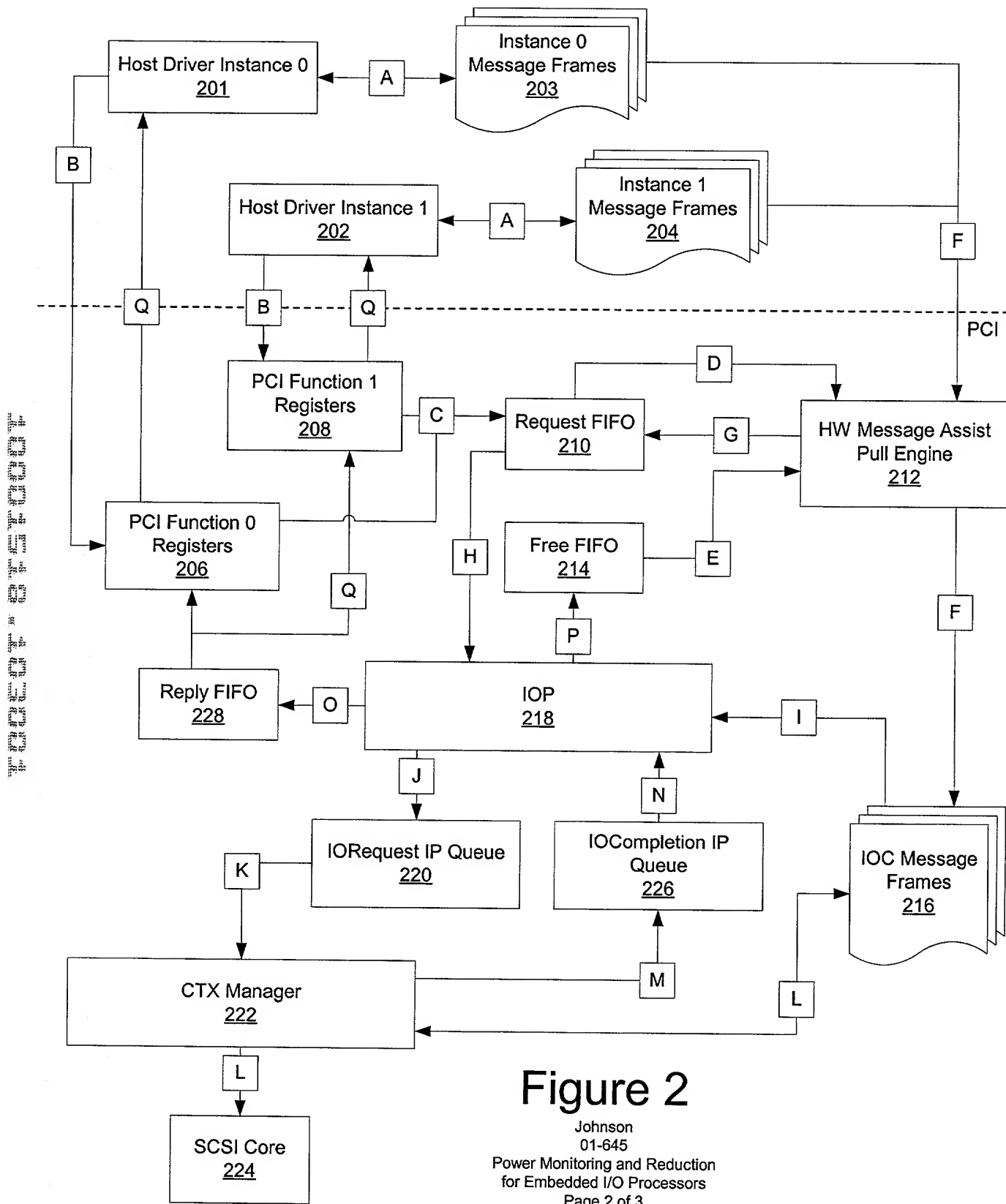


Figure 2

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Figure 3

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Power Monitoring and Reduction
for Embedded I/O Processors

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